Test Report: LDH-45A-700

45W DC-DC Step-Up Constant Current LED Driver

■ DESIGN VERIFY TEST

Output Function Test
Input Function Test
Control Function Test
Protection Function Test
Component Stress Test

■ E.M.C. TEST

E.M.C. Test

■ RELIABILITY TEST

ENVIRONMENT TEST

■ DESIGN VERIFY TEST

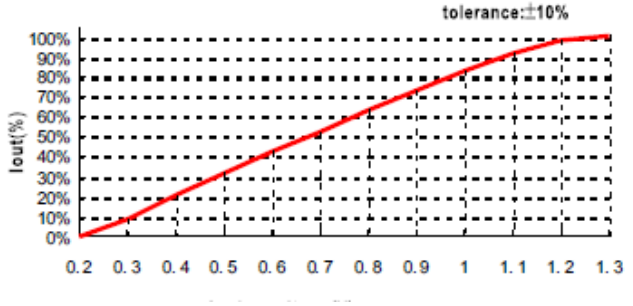
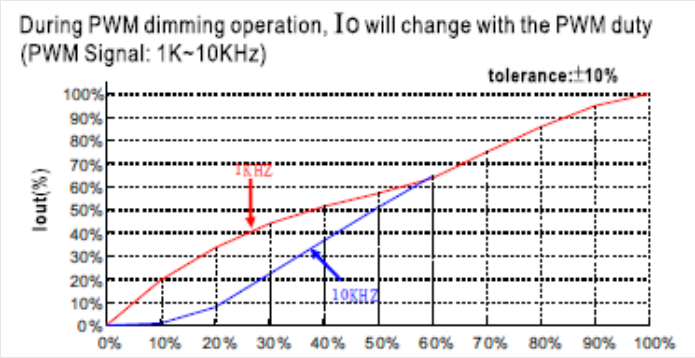
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	RIPPLE & NOISE	V1：1900 mVp-p (Max)	I/P： 12VDC O/P：FULL LOAD Ta：25℃	V1： 860 mVp-p (Max)	PASS
2	OUTPUT VOLTAGE RANGE	V1 = 12 V ~ 64 V	I/P： 9 VDC I/P： 12 VDC I/P： 18 VDC O/P：CV MODE Ta：25℃	O/P= 12V: 0.7165 A 9VDC O/P= 64V: 0.7167 A 9VDC O/P= 15V: 0.7168 A 12VDC O/P= 64V: 0.7173 A 12VDC O/P= 21V: 0.7173 A 18VDC O/P= 64V: 0.7179 A 18VDC	PASS
3	NO LOAD OUTPUT VOLTAGE	< 75 V	I/P： 12 VDC O/P：NO LOAD Ta：25℃	TEST： < 75 V	PASS
4	CURRENT ACCURACY	± 5%	I/P：12 VDC O/P：FULL LOAD Ta：25℃	TEST： ± 2.56 %	PASS

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	INPUT VOLTAGE RANGE	9VDC~18VDC	I/P：TESTING O/P：FULL LOAD Ta：25℃	8.8 V~ 18 V	PASS
			I/P： LOW-LINE-0.2V=8.8 V HIGH-LINE=18 V O/P：FULL/MIN LOAD ON： 30 Sec . OFF： 30 Sec 10MIN (AC POWER ON/OFF NO DAMAGE)	TEST： OK	
2	EFFICIENCY	90 % (TYP)	I/P： 12 VDC O/P：FULL LOAD Ta：25℃	91.78 %	PASS
3	DC CURRENT	12VDC/ 4.2 A (TYP)	I/P： 12 VDC O/P：FULL LOAD Ta：25℃	I = 3.93 A/ 12 VDC	PASS

CONTROL FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	DIMMING OFF	INPUT CURRENT＜ 7mA	I/P:12VDC O/P:FULL LOAD Ta:25℃	TEST： 4 mA	PASS
2	ANALOG DIMMING	SPEC: *Output constant current level can be adjusted through output cable by 0.2V~8Vdc DIM (+) and DIM (-). *0.2V~8V dimming function for output current adjustment (Typical) During analog dimming operation, Io will change with DC input voltage  			PASS
3	PWM DIMMING	SPEC: *Output constant current level can be adjusted through output cable by PWM signal DIM (+) and DIM (-). *2V~8V 1KHz~10KHz PWM signal for output current adjustment (Typical)  			PASS

TEST RESULT: I/P：12 VDC ;Ta：25℃

DIMMING	0.2V	0.3V	0.4V	0.5V	0.6V	0.7V	0.8V	0.9V	1.0V	1.1V	1.2V	1.3V	8.0V
O/P LOAD	0%	7.5%	19%	30%	41%	52%	63%	73%	83%	93%	99%	101%	101%

TEST RESULT:

I/P：12 VDC ;PWM Signal:1KHz ; Ta：25℃

DIMMING	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
O/P LOAD	19.40%	34.47%	45.46%	53.37%	59.24%	63.73%	71.59%	83.10%	93.39%	99.09%

I/P：12 VDC ;PWM Signal:10KHz ; Ta：25℃

DIMMING	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
O/P LOAD	0%	6.79%	21.81%	36.24%	50.54%	64.51%	78.19%	89.80%	96.56%	99.17%

PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	OVER VOLTAGE PROTECTION	CH1: ＜ 75 V	I/P: 9 VDC I/P: 12VDC I/P: 18VDC O/P:MIN LOAD Ta:25℃	69.33V /9 VDC 69.36V /12VDC 69.40V/18VDC Hold ON	PASS
2	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P： 18 VDC O/P：FULL LOAD Ta：25℃	NO DAMAGE Fuse Open	PASS

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	Power Transistor (D to S) or (C to E) Peak Voltage	Q2 Rated 150 V/ 33 A	I/P：High-Line +3V = 21 V O/P： (1)Full Load Turn on (2)Full load continue Ta：25℃	(1) 74.4 V (2) 73.6 V	PASS
2	Diode Peak Voltage	D1 Rated 150 V/ 10 A	I/P：High-Line +3V = 21 V O/P： (1)Full Load Turn on (2)Full load continue Ta：25℃	(1) 69.6 V (2) 68.8	PASS

■ E.M.C. TEST

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	RADIATION	EN55015	I/P: 12 VDC O/P: FULL LOAD Ta:25℃	PASS Test by certified Lab	PASS
2	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR:8KV / Contact:4KV	I/P: 12 VDC O/P:FULL LOAD Ta:25℃	CRITERIA A	PASS
3	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 12 VDC O/P:FULL LOAD Ta:25℃	CRITERIA A	PASS
4	Test by certified Lab & Test Report Prepare				

■ RELIABILITY TEST

ENVIRONMENT TEST

<table>
<tr><th>NO</th><th>TEST ITEM</th><th>SPECIFICATION</th><th>TEST CONDITION</th><th>RESULT</th><th>VERDICT</th></tr>
<tr><td>1</td><td>TEMPERATURE RISE TEST</td><td colspan="3">MODEL： LDH-45A-1050
1. ROOM AMBIENT BURN-IN：1.0 HRS
I/P： 12VDC O/P： LED LOAD=42.48V Ta=30.2 ℃
2. HIGH AMBIENT BURN-IN： 1.0 HRS
I/P： 12VDC O/P： LED LOAD=42.47V Ta=65.1 ℃
<table>
<tr><th>NO</th><th>Position</th><th>ROOM AMBIENT
Ta= 30.2 ℃</th><th>HIGH AMBIENT
Ta= 65.1 ℃</th></tr>
<tr><td>1</td><td>C1</td><td>49.7℃</td><td>86.1℃</td></tr>
<tr><td>2</td><td>LF1</td><td>51.9℃</td><td>88.8℃</td></tr>
<tr><td>3</td><td>C3</td><td>55.1℃</td><td>92.6℃</td></tr>
<tr><td>4</td><td>L1</td><td>60.0℃</td><td>98.5℃</td></tr>
<tr><td>5</td><td>U1</td><td>52.0℃</td><td>87.4℃</td></tr>
<tr><td>6</td><td>Q2</td><td>70.0℃</td><td>108.5℃</td></tr>
<tr><td>7</td><td>R7</td><td>63.2℃</td><td>100.4℃</td></tr>
<tr><td>8</td><td>R18</td><td>58.4℃</td><td>94.4℃</td></tr>
<tr><td>9</td><td>D2</td><td>55.7℃</td><td>91.6℃</td></tr>
<tr><td>10</td><td>D1</td><td>55.9℃</td><td>91.6℃</td></tr>
<tr><td>11</td><td>C5</td><td>63.8℃</td><td>100.6℃</td></tr>
<tr><td>12</td><td>C8</td><td>54.1℃</td><td>89.5℃</td></tr>
</table></td><td>P</td></tr>
<tr><td>2</td><td>LOW TEMPERATURE
TURN ON TEST</td><td>TURN ON AFTER 2 HOUR</td><td>I/P： 18VDC/9VDC
O/P：LED LOAD=43V
Ta= -45℃</td><td>TEST：OK</td><td>P</td></tr>
<tr><td>3</td><td>TEMPERATURE
COEFFICIENT</td><td>± 0.03 %(0~50℃)</td><td>I/P： 12VDC
O/P：LED LOAD=43V</td><td>± 0.0007%(0~50℃)</td><td>P</td></tr>
<tr><td>4</td><td>STORAGE TEMPERATURE TEST</td><td colspan="2">1. Thermal shock Temperature： -45℃~ +90℃
2. Temperature change rate ：25℃ / MIN
3. Dwell time low and high temperature ： 30 MIN/EACH
4. Total test cycle：5 CYCLE
5. Input/Output condition： STATIC</td><td>OK</td><td>P</td></tr>
<tr><td>5</td><td>THERMAL SHOCK TEST</td><td colspan="2">1. Thermal shock Temperature： -45℃~ +65℃
2. Temperature change rate ：25℃ / MIN
3. Dwell time low and high temperature ： 30 MIN/EACH
4. Total test cycle：10 CYCLE
5. Input/Output condition： 12VDC/ LED LOAD=43V DC ON/OFF TEST
turn on 58sec；turn off 2sec</td><td>OK</td><td>P</td></tr>
<tr><td>6</td><td>VIBRATION TEST</td><td colspan="2">1 Carton & 1 Set
(1) Waveform： Sine Wave
(2) Frequency：10~500Hz
(3) Sweep Time：10min/sweep cycle
(4) Acceleration：3G
(5) Test Time：90min in each axis (X.Y.Z)
(6) Ta：25℃</td><td>TEST：OK</td><td>P</td></tr>
</table>

7	CAPACITOR LIFE CYCLE	LDH-45A-1050:SUPPOSE C5 IS THE MOST CRITICAL COMPONENT (1) I/P： 12VDC O/P：FULL LOAD Ta=25℃ LIFE TIME (2) I/P： 12VDC O/P：FULL LOAD Ta=60℃ LIFE TIME (3) I/P： 12VDC O/P：75% LOAD Ta=60℃ LIFE TIME	(1) 298910.7 HRS (2) 23140.7 HRS (3) 44123.2 HRS	P
8	MTBF	MIL-HDBK-217F NOTICES2 PARTS COUNT TOTAL FAILURE RATE：1179.3KHRS		P
9	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure(Expected Life) : 30,000 hours @ Tcase 70℃ ; 50,000 hours @ Tcase 60℃		P

SAMPLE	TEST RESULT	TESTER	APPROVAL
PRODUCT SAMPLE	PASS	ZHUOKB/ZOULF	LIUWY

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